



*European Network on High Performance and
Embedded Architecture and Compilation*

Activities

HiPEAC

HiPEAC press release

[« Back to press room \(/press/releases/\)](#)

Jan. 15, 2018

HiPEAC18 provides the expertise needed to advance computing in the face of crisis

- Manchester is this year's setting for Europe's largest computing research conference, which takes place from 22-24 January
- As the world reels from the exposure of major chip-level security flaws, HiPEAC's community of experts is more essential than ever
- Three keynote talks, 27 workshops, eight tutorials and 33 papers over the three-day conference will cover everything from machine learning to secure critical real-time systems

Ghent, 15 January 2018 –From 22-24 January in Manchester, the [HiPEAC conference \(https://www.hipeac.net/2018/manchester/\)](https://www.hipeac.net/2018/manchester/) will once again bring together the best minds in computer architecture and compilation to exploit the enormous potential of new computing paradigms while minimizing the very real risks. At a time of global crisis in computing systems, with chip-level security flaws exposing the vulnerability of our ever-more connected society and the end of Moore's Law threatening to slow the progress brought about by faster, cheaper, more powerful processing, HiPEAC's network of experts will once again showcase their solutions for everything from machine learning to secure critical real-time systems.

'The HiPEAC conference is the flagship networking event of our 2000-strong community of computing experts,' says HiPEAC coordinator Koen de Bosschere of Ghent University. 'This year we are very happy to have two leading European companies (ARM for mobile computing and DeepMind for deep learning) as the main sponsors of the event. They are creating the key technological components of future smart devices,' he adds.

Keynote talks from Maria Girone (CERN openlab) on computing challenges at the Large Hadron Collider, Dileep Bhandarkar (Qualcomm Datacenter Technologies) on emerging data centre trends and Dan Belov (DeepMind) on machine learning will kick off each day.

Further highlights from the conference include:

- SAFURE's solutions for safety and security 'by construction' (<https://www.hipeac.net/events/activities/7537/safure/>) in interconnected, mixed-critical, cyber-physical systems, such as connected vehicles. During this session, SYSGO will present secure update concepts addressing fundamental safety requirements such as non-interference with respect to non-updated parts.
- Coordinated European research paving the way towards exascale computing (<https://www.hipeac.net/events/activities/7521/exascalehpc/>).
- GoingArm workshop (<https://www.hipeac.net/events/activities/7552/going-arm/>) on applications for the low-power Arm platform, including insights into the brain-inspired SpiNNaker machine (<http://apt.cs.manchester.ac.uk/projects/SpiNNaker/>) and hands-on Arm demonstrations.
- Innovative interconnect solutions at the AISTECS workshop (<https://www.hipeac.net/events/activities/7516/aistecs/>), including the launch of prototype memory disaggregation for cloud services developed by IBM Research - Ireland, as described in this blog post and video (<https://www.ibm.com/blogs/research/2018/01/advancing-cloud-memory-disaggregation/>).
- The Heterogeneity Alliance, coordinated by the TANGO project, which aims to bring heterogeneous architecture in to mainstream markets.

Beyond academic excellence, the conference also facilitates the transformation of cutting-edge research results into market-ready innovations. As well as providing a hub for researchers, industry representatives and policy makers to exchange ideas, the conference features a specific TETRAMAX workshop on technology transfer. This follows the recent HiPEAC Tech Transfer Awards, which recognized ten projects where concrete research results have been made industrial practice.

HiPEAC18 will also once again feature HiPEAC's tailored recruitment support, including a travelling careers unit, which helps companies find candidates with the specialist skills to bring about the computing systems of the future. For the first time, the conference will also feature a STEM (Science, Technology, Engineering and Mathematics) Student Day, with the aim of preparing the next generation of computer scientists who will ensure Europe's enduring competitiveness. With the Manchester 'Baby', the world's first stored-program computer, celebrating its 70th birthday this year, the northern city provides a particularly apt location for the conference, which is testament to the power of collaborative European research in the face of political uncertainty.

Once again, the biggest international names in technology, including Arm, DeepMind, Atos and Samsung, have shown their confidence in HiPEAC by generously supporting the conference. Full list of sponsors below.

View the [programme on the HiPEAC18 website](http://www.hipeac.net/2018/manchester/schedule)

<http://www.hipeac.net/2018/manchester/schedule> – note that you can filter sessions by smart application area and/or technology focus.

Social media hashtag: [#HiPEAC18](https://twitter.com/search?f=tweets&vertical=default&q=#HiPEAC18) (<https://twitter.com/search?f=tweets&vertical=default&q=#HiPEAC18>)

In this news

[\(/network/institutions/257/arm/\)](#) [\(/network/institutions/7281/artemis-industry-association/\)](#)

[\(/network/institutions/7528/atos/\)](#) [\(/network/institutions/758/barcelona-supercomputing-center-bsc/\)](#)

[\(/network/institutions/3623/barco/\)](#) [\(/network/institutions/2325/cea/\)](#)

[\(/network/institutions/231/chalmers-university-of-technology/\)](#) [\(/network/institutions/7922/deepmind/\)](#)

[\(/network/institutions/7395/dividit/\)](#) [\(/network/institutions/7127/embedded-computing-specialists/\)](#)

[\(/network/institutions/280/foundation-for-research-technology-hellas-forth/\)](#) [\(/network/institutions/237/ghent-university/\)](#)

[\(/network/institutions/7886/innovae/\)](#) [\(/network/institutions/278/inria/\)](#)

[\(/network/institutions/7595/jemm/\)](#) [\(/network/institutions/7503/kaleao/\)](#) [research/\)](#)

</network/institutions/239/rwth-aachen-university/> </network/institutions/7894/samsung/>

</network/institutions/7942/springer/> </network/institutions/7533/sundance-multiprocessor-technology/>

</network/institutions/4108/sysgo/> </network/institutions/1057/thales-research-and-technology/>

</network/institutions/254/the-university-of-edinburgh/> </network/institutions/4848/think-silicon/>

</network/institutions/242/university-of-pisa/>

About HiPEAC

Since 2004, the HiPEAC (High Performance and Embedded Architecture and Compilation) project has provided a hub for European researchers in computing systems; today, its network, the biggest of its kind in the world, numbers around 1,500 specialists. The project offers training, mobility support and dissemination and recruitment services, along with numerous networking facilities to its members. The latest incarnation of the project, HiPEAC 4, was launched on 1 January 2016 and is delivered by 12 partners, led by Ghent University. It is a Coordination and Support Action funded by the European Union's Horizon 2020 research and innovation programme under grant agreement no. 687698.



Press contact: [Madeleine Gray \(mailto:communication@hipeac.net\)](mailto:communication@hipeac.net), Communication Officer

Share

Tweet

© 2004-2018 **HiPEAC**

The HiPEAC project has received funding from the European Union's Horizon2020 research and innovation programme under grant agreement number 687698.